

In the Claims:

Please cancel claims 155, 156, 162, 163, 169, 170, 173, 179, 180, 186, 187, 190, 196, 197, 203, 204, 207, 213, 214, 220, 221, 227, 228, 234, 235, 241, 242, 245, 251, 252, 258, 259, 262, 268, 269, 275, 276, 279, 285, 286, 292 and 293 without prejudice.

Please amend claims 1-9, 11-154, 157-161, 164-168, 172, 222-226, 229-233, 236-240, 243, 244, 246-250, 253-257, 260, 261, 263-267, 270-274, 277, 278, 280-284 and 287-291. Pursuant to the revised format for amendments, the status of all of the claims is set forth below.

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AW B1
1. (Currently Amended) A memory address space extension device comprising:

a definition table defining an access right of a task ID (identification) with respect to a data area;

a task ID register for storing therein the task ID of a task during execution;

an extraction means for extracting a data area ID from a CPU (central processing unit) address inputted previously;

a judgement means for judging as to whether or not it is capable of being permitted that the task ~~during execution~~ indicated by the task ID accesses the data area indicated by the data area ID, ~~the judgement being made with reference while referring to said definition table from both of data area ID extracted previously and the task ID stored in said task ID register, and said judgement means judges whether or not said task is permitted access according to any one of said task ID and an interruption processing number and/or a data area ID indicating the data area requested by said task.~~

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2. (Currently Amended) A memory address space extension device as claimed in claim 1, ~~wherein there are provided further comprising~~ an address operational means for converting said CPU address into an extended address, and a memory having the extended address converted previously, ~~and when said judgement means judges that judgement result according to said judgement means is a result that the task during execution accesses the data area indicated by the data area ID, said address operational means executes said converting of said CPU address conversion processing while causing said memory with extended address to support.~~

3. (Currently Amended) A memory address space extension device as claimed in claim 1, wherein, ~~as to said definition table, it causes combination that defines whether access is permissible or in-permissible impermissible with respect to a read and / or write operation to said data area respectively to be defined.~~

4. (Currently Amended) A memory address space extension device as claimed in claim 1, ~~wherein there is provided further comprising~~ a preparation means for preparing said definition table.

5. (Currently Amended) A storage medium for storing therein a program in order to execute the processing of:

a storing processing for storing a task ID of a task during execution;

an extraction processing for extracting a data area ID from a CPU address inputted previously; and

a processing for judging as to whether or not ~~it is capable of being permitted that the task during execution indicated by the task ID accesses the data area indicated by the data area ID, the judgement being made with reference while referring to said definition table from both of data area ID extracted previously and the task ID stored in said task ID register, and said processing for judging judges whether or not said task is~~

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permitted access according to any one of said task ID and an interruption processing number and/or a data area ID indicating the data area requested by said task.

6. (Currently Amended) A storage medium for storing therein a program as claimed in claim 5, wherein said program executes the processing of an address operational processing for converting said CPU address into an extended address when a judgement result according to said ~~judgement~~ judging processing is that the task is permitted ~~that the task during execution accesses the data area to access the data during execution.~~

7. (Currently Amended) A memory address space extension device comprising:

~~a definition table forming means for forming said definition table;~~

a definition table for defining an access right of a task with respect to a data area;

a definition table forming means for forming said definition table; and

an access right judgement means for judging, according to said definition table, whether or not it causes the access right to the data area requested by the task to should be given-provided with respect to the task according to said definition table, and said access right judgement means judges whether or not said task is permitted access according to any one of a task ID and an interruption processing number and/or a data area ID indicating the data area requested by said task.

8. (Currently Amended) A memory address space extension device as claimed in claim 7, wherein said definition table defines ~~as to~~ whether or not said task possesses an access right for accessing a prescribed data area in every task ID ~~given to~~ associated with the task.

9. (Currently Amended) A memory address space extension device as claimed in claim 7, wherein said definition table defines ~~as to~~ whether or not said task possesses an

Al^o access right for accessing a prescribed data area in every task ID ~~given to~~ associated with the task and / or in every interruption number given to interruption processing.

10. (Original) A memory address space extension device as claimed in claim 7, wherein said access right judgement means judges whether it causes the access right to the data area requested by said task to be given according to said task ID, and the data area ID indicating the data area requested by said task.

11. (Currently Amended) A memory address space extension device as claimed in claim 8, wherein said access right judgement means judges whether ~~it causes the to~~ permit access ~~right~~ to the data area requested by said task ~~to be given~~ according to said task ID, and the data area ID indicating the data area requested by said task.

12. (Currently Amended) A memory address space extension device as claimed in claim 7, wherein said access right judgement means judges whether ~~it causes the to~~ permit access ~~right~~ to the data area requested by said task ~~to be given~~ according to any one of said task ID and said interruption processing number, and according to the data area ID indicating the data area requested by said task.

13. (Currently Amended) A memory address space extension device as claimed in claim 9, wherein said access right judgement means judges whether ~~it causes the to~~ permit access ~~right~~ to the data area requested by said task ~~to be given~~ according to any one of said task ID and said interruption processing number, and according to the data area ID indicating the data area requested by said task.

14. (Currently Amended) A memory address space extension device as claimed in claim 7, wherein said definition table defines whether or not ~~it causes access to be~~ is permitted with respect to said prescribed data area in every said task.

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15. (Currently Amended) A memory address space extension device as claimed in claim 8, wherein said definition table defines whether or not ~~it causes access to be~~ is permitted with respect to said prescribed data area in every said task.

16. (Currently Amended) A memory address space extension device as claimed in claim 9, wherein said definition table defines whether or not ~~it causes access to be~~ is permitted with respect to said prescribed data area in every said task.

17. (Currently Amended) A memory address space extension device as claimed in claim 10, wherein said definition table defines whether or not ~~it causes access to be~~ is permitted with respect to said prescribed data area in every said task.

18. (Currently Amended) A memory address space extension device as claimed in claim 11, wherein said definition table defines whether or not ~~it causes access to be~~ is permitted with respect to said prescribed data area in every said task.

19. (Currently Amended) A memory address space extension device as claimed in claim 12, wherein said definition table defines whether or not ~~it causes access to be~~ is permitted with respect to said prescribed data area in every said task.

20. (Currently Amended) A memory address space extension device as claimed in claim 13, wherein said definition table defines whether or not ~~it causes access to be~~ is permitted with respect to said prescribed data area in every said task.

21. (Currently Amended) A memory address space extension device as claimed in claim 7, wherein said definition table defines any one of:

- (1) causing only ~~write~~ writing of the data to be permitted;
- (2) causing ~~read~~ reading of the data to be permitted;

A10 (3) causing ~~write~~ writing of the data and ~~read~~ reading of the data to be permitted; and

(4) causing no-access to be permitted,

with respect to said prescribed data area in every said task.

22. (Currently Amended) A memory address space extension device as claimed in claim 7 8, wherein said definition table defines any one of:

(1) causing only ~~write~~ writing of the data to be permitted;

(2) causing ~~read~~ reading of the data to be permitted;

(3) causing ~~write~~ writing of the data and ~~read~~ reading of the data to be permitted; and

(4) causing no-access to be permitted,

with respect to said prescribed data area in every said task.

23. (Currently Amended) A memory address space extension device as claimed in claim 7 9, wherein said definition table defines any one of:

(1) causing only ~~write~~ writing of the data to be permitted;

(2) causing ~~read~~ reading of the data to be permitted;

(3) causing ~~write~~ writing of the data and ~~read~~ reading of the data to be permitted; and

(4) causing no-access to be permitted,

with respect to said prescribed data area in every said task.

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24. (Currently Amended) A memory address space extension device as claimed in claim 7 10, wherein said definition table defines any one of:

- (1) causing only ~~write~~ writing of the data to be permitted;
 - (2) causing ~~read~~ reading of the data to be permitted;
 - (3) causing ~~write~~ writing of the data and ~~read~~ reading of the data to be permitted; and
 - (4) causing no-access to be permitted,
- with respect to said prescribed data area in every said task.

25. (Currently Amended) A memory address space extension device as claimed in claim 7 11, wherein said definition table defines any one of:

- (1) causing only ~~write~~ writing of the data to be permitted;
 - (2) causing ~~read~~ reading of the data to be permitted;
 - (3) causing ~~write~~ writing of the data and ~~read~~ reading of the data to be permitted; and
 - (4) causing no-access to be permitted,
- with respect to said prescribed data area in every said task.

26. (Currently Amended) A memory address space extension device as claimed in claim 7 12, wherein said definition table defines any one of:

- (1) causing only ~~write~~ writing of the data to be permitted;
- (2) causing ~~read~~ reading of the data to be permitted;

A10 (3) causing ~~write~~ writing of the data and ~~read~~ reading of the data to be permitted; and

(4) causing no-access to be permitted,

with respect to said prescribed data area in every said task.

27. (Currently Amended) A memory address space extension device as claimed in claim 7 13, wherein said definition table defines any one of:

(1) causing only ~~write~~ writing of the data to be permitted;

(2) causing ~~read~~ reading of the data to be permitted;

(3) causing ~~write~~ writing of the data and ~~read~~ reading of the data to be permitted; and

(4) causing no-access to be permitted,

with respect to said prescribed data area in every said task.

28. (Currently Amended) A memory address space extension device as claimed in claim 9, ~~wherein there is further comprising~~ provided an interruption number generation means for generating interruption number with respect to interruption processing according to hardware.

29. (Currently Amended) A memory address space extension device as claimed in claim 12, ~~wherein there is further comprising~~ provided an interruption number generation means for generating interruption number with respect to interruption processing according to hardware.

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30. (Currently Amended) A memory address space extension device as claimed in claim 13, ~~wherein there is further comprising provided~~ an interruption number generation means for generating interruption number with respect to interruption processing according to hardware.

31. (Currently Amended) A memory address space extension device as claimed in claim 14, ~~wherein there is further comprising provided~~ an interruption number generation means for generating interruption number with respect to interruption processing according to hardware.

32. (Currently Amended) A memory address space extension device as claimed in claim 15, ~~wherein there is further comprising provided~~ an interruption number generation means for generating interruption number with respect to interruption processing according to hardware.

33. (Currently Amended) A memory address space extension device as claimed in claim 16, ~~wherein there is further comprising provided~~ an interruption number generation means for generating interruption number with respect to interruption processing according to hardware.

34. (Currently Amended) A memory address space extension device as claimed in claim 17, ~~wherein there is further comprising provided~~ an interruption number generation means for generating interruption number with respect to interruption processing according to hardware.

35. (Currently Amended) A memory address space extension device as claimed in claim 18, ~~wherein there is further comprising provided~~ an interruption number generation means for generating interruption number with respect to interruption processing according to hardware.

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36. (Currently Amended) A memory address space extension device as claimed in claim 19, ~~wherein there is further comprising provided~~ an interruption number generation means for generating interruption number with respect to interruption processing according to hardware.

37. (Currently Amended) A memory address space extension device as claimed in claim 20, ~~wherein there is further comprising provided~~ an interruption number generation means for generating interruption number with respect to interruption processing according to hardware.

38. (Currently Amended) A memory address space extension device as claimed in claim 21, ~~wherein there is further comprising provided~~ an interruption number generation means for generating interruption number with respect to interruption processing according to hardware.

39. (Currently Amended) A memory address space extension device as claimed in claim 22, ~~wherein there is further comprising provided~~ an interruption number generation means for generating interruption number with respect to interruption processing according to hardware.

40. (Currently Amended) A memory address space extension device as claimed in claim 23, ~~wherein there is further comprising provided~~ an interruption number generation means for generating interruption number with respect to interruption processing according to hardware.

41. (Currently Amended) A memory address space extension device as claimed in claim 24, ~~wherein there is further comprising provided~~ an interruption number generation means for generating interruption number with respect to interruption processing according to hardware.

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42. (Currently Amended) A memory address space extension device as claimed in claim 25, ~~wherein there is further comprising provided~~ an interruption number generation means for generating interruption number with respect to interruption processing according to hardware.

43. (Currently Amended) A memory address space extension device as claimed in claim 26, ~~wherein there is further comprising provided~~ an interruption number generation means for generating interruption number with respect to interruption processing according to hardware.

44. (Currently Amended) A memory address space extension device as claimed in claim 27, ~~wherein there is further comprising provided~~ an interruption number generation means for generating interruption number with respect to interruption processing according to hardware.

45. (Currently Amended) A memory address space extension device as claimed in claim 9, ~~wherein there is further comprising provided~~ an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

46. (Currently Amended) A memory address space extension device as claimed in claim 12, ~~wherein there is further comprising provided~~ an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

47. (Currently Amended) A memory address space extension device as claimed in claim 13, ~~wherein there is further comprising provided~~ an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

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49. (Currently Amended) A memory address space extension device as claimed in claim 15, ~~wherein there is further comprising provided~~ an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

50. (Currently Amended) A memory address space extension device as claimed in claim 16, ~~wherein there is further comprising provided~~ an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

51. (Currently Amended) A memory address space extension device as claimed in claim 17, ~~wherein there is further comprising provided~~ an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

52. (Currently Amended) A memory address space extension device as claimed in claim 18, ~~wherein there is further comprising provided~~ an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

53. (Currently Amended) A memory address space extension device as claimed in claim 19, ~~wherein there is further comprising provided~~ an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

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54. (Currently Amended) A memory address space extension device as claimed in claim 20, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

55. (Currently Amended) A memory address space extension device as claimed in claim 21, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

56. (Currently Amended) A memory address space extension device as claimed in claim 22, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

57. (Currently Amended) A memory address space extension device as claimed in claim 23, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

58. (Currently Amended) A memory address space extension device as claimed in claim 24, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

59. (Currently Amended) A memory address space extension device as claimed in claim 25, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

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60. (Currently Amended) A memory address space extension device as claimed in claim 26, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

61. (Currently Amended) A memory address space extension device as claimed in claim 27, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

62. (Currently Amended) A memory address space extension device as claimed in claim 28, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

63. (Currently Amended) A memory address space extension device as claimed in claim 29, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

64. (Currently Amended) A memory address space extension device as claimed in claim 30, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

65. (Currently Amended) A memory address space extension device as claimed in claim 31, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

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66. (Currently Amended) A memory address space extension device as claimed in claim 32, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

67. (Currently Amended) A memory address space extension device as claimed in claim 33, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

68. (Currently Amended) A memory address space extension device as claimed in claim 34, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

69. (Currently Amended) A memory address space extension device as claimed in claim 35, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

70. (Currently Amended) A memory address space extension device as claimed in claim 36, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

71. (Currently Amended) A memory address space extension device as claimed in claim 37, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

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72. (Currently Amended) A memory address space extension device as claimed in claim 38, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

73. (Currently Amended) A memory address space extension device as claimed in claim 39, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

74. (Currently Amended) A memory address space extension device as claimed in claim 40, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

75. (Currently Amended) A memory address space extension device as claimed in claim 41, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

76. (Currently Amended) A memory address space extension device as claimed in claim 42, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

77. (Currently Amended) A memory address space extension device as claimed in claim 43, ~~wherein there is further comprising~~ provided an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

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78. (Currently Amended) A memory address space extension device as claimed in claim 44, ~~wherein there is further comprising provided~~ an interruption number decoding means for decoding an interruption number from an interruption instruction with respect to said interruption instruction according to software.

79. (Currently Amended) A memory address space extension device as claimed in claim 7, ~~wherein there is further comprising provided~~ an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

80. (Currently Amended) A memory address space extension device as claimed in claim 8, ~~wherein there is further comprising provided~~ an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

81. (Currently Amended) A memory address space extension device as claimed in claim 9, ~~wherein there is further comprising provided~~ an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

82. (Currently Amended) A memory address space extension device as claimed in claim 10, ~~wherein there is further comprising provided~~ an address operational means for

A10 converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

83. (Currently Amended) A memory address space extension device as claimed in claim 11, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

84. (Currently Amended) A memory address space extension device as claimed in claim 12, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

85. (Currently Amended) A memory address space extension device as claimed in claim 13, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

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86. (Currently Amended) A memory address space extension device as claimed in claim 14, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

87. (Currently Amended) A memory address space extension device as claimed in claim 15, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

88. (Currently Amended) A memory address space extension device as claimed in claim 16, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

89. (Currently Amended) A memory address space extension device as claimed in claim 17, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to

said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

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90. (Currently Amended) A memory address space extension device as claimed in claim 18, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

91. (Currently Amended) A memory address space extension device as claimed in claim 19, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

92. (Currently Amended) A memory address space extension device as claimed in claim 20, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

93. (Currently Amended) A memory address space extension device as claimed in claim 21, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU

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address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

94. (Currently Amended) A memory address space extension device as claimed in claim 22, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

95. (Currently Amended) A memory address space extension device as claimed in claim 23, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

96. (Currently Amended) A memory address space extension device as claimed in claim 24, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

97. (Currently Amended) A memory address space extension device as claimed in claim 25, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended

A10 address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

98. (Currently Amended) A memory address space extension device as claimed in claim 26, ~~wherein there is further~~ comprising ~~provided~~ an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

99. (Currently Amended) A memory address space extension device as claimed in claim 27, ~~wherein there is further~~ comprising ~~provided~~ an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

100. (Currently Amended) A memory address space extension device as claimed in claim 28, ~~wherein there is further~~ comprising ~~provided~~ an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

101. (Currently Amended) A memory address space extension device as claimed in claim 29, ~~wherein there is further~~ comprising ~~provided~~ an address operational

A10 means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

102. (Currently Amended) A memory address space extension device as claimed in claim 30, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

103. (Currently Amended) A memory address space extension device as claimed in claim 31, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

104. (Currently Amended) A memory address space extension device as claimed in claim 32, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

A10 105. (Currently Amended) A memory address space extension device as claimed in claim 33, ~~wherein there is further~~ comprising ~~provided~~ an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

106. (Currently Amended) A memory address space extension device as claimed in claim 34, ~~wherein there is further~~ comprising ~~provided~~ an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

107. (Currently Amended) A memory address space extension device as claimed in claim 35, ~~wherein there is further~~ comprising ~~provided~~ an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

108. (Currently Amended) A memory address space extension device as claimed in claim 36, ~~wherein there is further~~ comprising ~~provided~~ an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to

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access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

109. (Currently Amended) A memory address space extension device as claimed in claim 37, ~~wherein there is further comprising provided~~ an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

110. (Currently Amended) A memory address space extension device as claimed in claim 38, ~~wherein there is further comprising provided~~ an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

111. (Currently Amended) A memory address space extension device as claimed in claim 39, ~~wherein there is further comprising provided~~ an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

112. (Currently Amended) A memory address space extension device as claimed in claim 41, ~~wherein there is further comprising provided~~ an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes

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said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

113. (Currently Amended) A memory address space extension device as claimed in claim 42, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

114. (Currently Amended) A memory address space extension device as claimed in claim 43, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

115. (Currently Amended) A memory address space extension device as claimed in claim 44, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

116. (Currently Amended) A memory address space extension device as claimed in claim 45, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having

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the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

117. (Currently Amended) A memory address space extension device as claimed in claim 46, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

118. (Currently Amended) A memory address space extension device as claimed in claim 47, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

119. (Currently Amended) A memory address space extension device as claimed in claim 48, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

120. (Currently Amended) A memory address space extension device as claimed in claim 49, ~~wherein there is further comprising~~ provided an address operational

A10 means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

121. (Currently Amended) A memory address space extension device as claimed in claim 50, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

122. (Currently Amended) A memory address space extension device as claimed in claim 51, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

123. (Currently Amended) A memory address space extension device as claimed in claim 52, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

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124. (Currently Amended) A memory address space extension device as claimed in claim 53, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

125. (Currently Amended) A memory address space extension device as claimed in claim 54, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

126. (Currently Amended) A memory address space extension device as claimed in claim 55, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

127. (Currently Amended) A memory address space extension device as claimed in claim 56, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to

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access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

128. (Currently Amended) A memory address space extension device as claimed in claim 58, ~~wherein there is further comprising provided~~ an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

129. (Currently Amended) A memory address space extension device as claimed in claim 59, ~~wherein there is further comprising provided~~ an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

130. (Currently Amended) A memory address space extension device as claimed in claim 60, ~~wherein there is further comprising provided~~ an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

131. (Currently Amended) A memory address space extension device as claimed in claim 61, ~~wherein there is further comprising provided~~ an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes

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said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

132. (Currently Amended) A memory address space extension device as claimed in claim 62, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

133. (Currently Amended) A memory address space extension device as claimed in claim 63, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

134. (Currently Amended) A memory address space extension device as claimed in claim 64, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

135. (Currently Amended) A memory address space extension device as claimed in claim 65, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having

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the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

136. (Currently Amended) A memory address space extension device as claimed in claim 66, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

137. (Currently Amended) A memory address space extension device as claimed in claim 67, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

138. (Currently Amended) A memory address space extension device as claimed in claim 68, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

139. (Currently Amended) A memory address space extension device as claimed in claim 69, ~~wherein there is further comprising~~ provided an address operational

A10 means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

140. (Currently Amended) A memory address space extension device as claimed in claim 70, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

141. (Currently Amended) A memory address space extension device as claimed in claim 71, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

142. (Currently Amended) A memory address space extension device as claimed in claim 72, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

AP 143. (Currently Amended) A memory address space extension device as claimed in claim 73, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

144. (Currently Amended) A memory address space extension device as claimed in claim 74, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

145. (Currently Amended) A memory address space extension device as claimed in claim 75, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

146. (Currently Amended) A memory address space extension device as claimed in claim 76, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to

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access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

147. (Currently Amended) A memory address space extension device as claimed in claim 77, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

148. (Currently Amended) A memory address space extension device as claimed in claim 78, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

149. (Currently Amended) A memory address space extension device as claimed in claim 79, ~~wherein there is further comprising~~ provided an address operational means for converting the CPU address into the extended address, and a memory having the extended address converted previously wherein said address operational means causes said CPU address to be converted into the extended address when said task is permitted to access to said data area according to said access right judgement means, before said memory with extended address supports said address operational means.

150. (Currently Amended) A storage medium storing therein a program in order to execute the processing of:

a definition table preparation processing for preparing said a definition table;

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a reference processing for said definition table which defines an access right of a task with respect to a data area; and

an access right judgement processing for judging whether or not said task is permitted ~~it causes the access right~~ for the data area requested by a said task to be given to said task according to said definition table, and said access right judgement processing judges whether or not said task is permitted access according to any one of a task ID and an interruption processing number and/or a data area ID indicating the data area requested by said task.

151. (Currently Amended) A storage medium storing therein a program as claimed in claim 150, wherein said reference processing for said definition table ~~is the reference processing that it causes the~~ said definition table defining to define whether or not said task possesses the access right for accessing a prescribed data area to be seen in every task ID ~~given to~~ associated with said task.

152. (Currently Amended) A storage medium storing therein a program as claimed in claim 150, wherein said reference processing for said definition table ~~is the reference processing that it causes the~~ definition table defining whether or not said task possesses the access right for accessing prescribed data area to be seen in every task ID given to said task and / or in every interruption number given to the interruption processing.

153. (Currently Amended) A storage medium storing therein a program as claimed in claim 150, wherein said access right judgement processing judges whether or not said task possesses the access right for accessing a prescribed data area according to said task ID and / or said data area ID indicating the data area requested by said task.

154. (Currently Amended) A storage medium storing therein a program as claimed in claim 151, wherein said access right judgement processing judges whether or

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not said task possesses the access right for accessing a prescribed data area according to said task ID and / or said data area ID indicating the data area requested by said task.

155. (Cancelled)

156. (Cancelled)

157. (Currently Amended) A storage medium storing therein a program as claimed in claim 150, wherein said reference processing for said definition table ~~is the reference processing that it causes the~~ defining to define whether or not said task possesses the access right for accessing a prescribed data area to be seen in every said task.

158. (Currently Amended) A storage medium storing therein a program as claimed in claim 151, wherein said reference processing for said definition table ~~is the reference processing that it causes~~ said the definition table defining to define whether or not said task possesses the access right for accessing a prescribed data area to be seen in every said task.

159. (Currently Amended) A storage medium storing therein a program as claimed in claim 152, wherein said reference processing for said definition table ~~is the reference processing that it causes~~ said the definition table defining to define whether or not said task possesses the access right for accessing a prescribed data area to be seen in every said task.

160. (Currently Amended) A storage medium storing therein a program as claimed in claim 153, wherein said reference processing for said definition table ~~is the reference processing that it causes~~ said the definition table defining to define whether or not said task possesses the access right for accessing a prescribed data area to be seen in every said task.

110 161. (Currently Amended) A storage medium storing therein a program as claimed in claim 154, wherein said reference processing for said definition table ~~is the reference processing that it causes~~ said the definition table defining to define whether or not said task possesses the access right for accessing prescribed data area to be seen in every said task.

162. (Cancelled)

163. (Cancelled)

164. (Currently Amended) A storage medium storing therein a program as claimed in claim 150, wherein said reference processing for said definition table ~~is the reference processing that it causes~~ any one of:

- (1) allowing only ~~write~~ writing of the data to be permitted;
 - (2) allowing ~~read~~ reading of the data to be permitted;
 - (3) allowing ~~write~~ writing of the data and ~~read~~ reading of the data to be permitted; and
 - (4) allowing no-access to be permitted,
- to be seen with respect to said prescribed data area in every said task.

165. (Currently Amended) A storage medium storing therein a program as claimed in claim 151, wherein said reference processing for said definition table ~~is the reference processing that it causes~~ any one of:

- (1) allowing only ~~write~~ writing of the data to be permitted;

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(2) allowing ~~read~~ reading of the data to be permitted;

(3) allowing ~~write~~ writing of the data and ~~read~~ reading of the data to be permitted; and

(4) allowing no-access to be permitted,

to be seen with respect to said prescribed data area in every said task.

166. (Currently Amended) A storage medium storing therein a program as claimed in claim 152, wherein said reference processing for said definition table ~~is the reference processing that it~~ causes any one of:

(1) allowing only ~~write~~ writing of the data to be permitted;

(2) allowing ~~read~~ reading of the data to be permitted;

(3) allowing ~~write~~ writing of the data and ~~read~~ reading of the data to be permitted; and

(4) allowing no-access to be permitted,

to be seen with respect to said prescribed data area in every said task.

167. (Currently Amended) A storage medium storing therein a program as claimed in claim 153, wherein said reference processing for said definition table ~~is the reference processing that it~~ causes any one of:

(1) allowing only ~~write~~ writing of the data to be permitted;

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(2) allowing ~~read~~ reading of the data to be permitted;

(3) allowing ~~write~~ writing of the data and ~~read~~ reading of the data to be permitted; and

(4) allowing no-access to be permitted,

to be seen with respect to said prescribed data area in every said task.

168. (Currently Amended) A storage medium storing therein a program as claimed in claim 154, wherein said reference processing for said definition table ~~is the reference processing that~~ it causes any one of:

(1) allowing only ~~write~~ writing of the data to be permitted;

(2) allowing ~~read~~ reading of the data to be permitted;

(3) allowing ~~write~~ writing of the data and ~~read~~ reading of the data to be permitted; and

(4) allowing no-access to be permitted,

to be seen with respect to said prescribed data area in every said task.

169. (Cancelled)

170. (Cancelled)

171. (Original) A storage medium storing therein a program as claimed in claim 152, wherein said program further executes interruption number generation processing for generating interruption number to interruption processing according to hardware.

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172. (Currently Amended) A storage medium storing therein a program as claimed in claim ~~155~~ 150, wherein said program further executes interruption number generation processing for generating interruption number to interruption processing according to hardware.

173. (Cancelled)

174. (Original) A storage medium storing therein a program as claimed in claim 157, wherein said program further executes interruption number generation processing for generating interruption number to interruption processing according to hardware.

175. (Original) A storage medium storing therein a program as claimed in claim 158, wherein said program further executes interruption number generation processing for generating interruption number to interruption processing according to hardware.

176. (Original) A storage medium storing therein a program as claimed in claim 159, wherein said program further executes interruption number generation processing for generating interruption number to interruption processing according to hardware.

177. (Original) A storage medium storing therein a program as claimed in claim 160, wherein said program further executes interruption number generation processing for generating interruption number to interruption processing according to hardware.

178. (Original) A storage medium storing therein a program as claimed in claim 161, wherein said program further executes interruption number generation processing for generating interruption number to interruption processing according to hardware.

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179. (Cancelled)

180. (Cancelled)

181. (Original) A storage medium storing therein a program as claimed in claim 164, wherein said program further executes interruption number generation processing for generating interruption number to interruption processing according to hardware.

182. (Original) A storage medium storing therein a program as claimed in claim 165, wherein said program further executes interruption number generation processing for generating interruption number to interruption processing according to hardware.

183. (Original) A storage medium storing therein a program as claimed in claim 166, wherein said program further executes interruption number generation processing for generating interruption number to interruption processing according to hardware.

184. (Original) A storage medium storing therein a program as claimed in claim 167, wherein said program further executes interruption number generation processing for generating interruption number to interruption processing according to hardware.

185. (Original) A storage medium storing therein a program as claimed in claim 168, wherein said program further executes interruption number generation processing for generating interruption number to interruption processing according to hardware.

186. (Cancelled)

187. (Cancelled)

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188. (Original) A storage medium storing therein a program as claimed in claim 152, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

189. (Currently Amended) A storage medium storing therein a program as claimed in claim ~~155~~ 150, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

190. (Cancelled)

191. (Original) A storage medium storing therein a program as claimed in claim 157, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

192. (Original) A storage medium storing therein a program as claimed in claim 158, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

193. (Original) A storage medium storing therein a program as claimed in claim 159, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

194. (Original) A storage medium storing therein a program as claimed in claim 160, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

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195. (Original) A storage medium storing therein a program as claimed in claim 161, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

196. (Cancelled)

197. (Cancelled)

198. (Original) A storage medium storing therein a program as claimed in claim 164, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

199. (Original) A storage medium storing therein a program as claimed in claim 165, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

200. (Original) A storage medium storing therein a program as claimed in claim 166, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

201. (Original) A storage medium storing therein a program as claimed in claim 167, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

202. (Original) A storage medium storing therein a program as claimed in claim 168, wherein said program further executes interruption number decoding

A10 processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

203. (Cancelled)

204. (Cancelled)

205. (Original) A storage medium storing therein a program as claimed in claim 171, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

206. (Original) A storage medium storing therein a program as claimed in claim 172, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

207. (Cancelled)

208. (Original) A storage medium storing therein a program as claimed in claim 174, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

209. (Original) A storage medium storing therein a program as claimed in claim 175, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

210. (Original) A storage medium storing therein a program as claimed in claim 176, wherein said program further executes interruption number decoding

A10 processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

211. (Original) A storage medium storing therein a program as claimed in claim 177, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

212. (Original) A storage medium storing therein a program as claimed in claim 178, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

213. (Cancelled)

214. (Cancelled)

215. (Original) A storage medium storing therein a program as claimed in claim 181, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

216. (Original) A storage medium storing therein a program as claimed in claim 182, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

217. (Original) A storage medium storing therein a program as claimed in claim 183, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

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218. (Original) A storage medium storing therein a program as claimed in claim 184, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

219. (Original) A storage medium storing therein a program as claimed in claim 185, wherein said program further executes interruption number decoding processing for decoding interruption number from interruption instruction with respect to said interruption instruction according to software.

220. (Cancelled)

221. (Cancelled)

222. (Currently Amended) A storage medium storing therein a program as claimed in claim 150, wherein said program executes an address operational processing in order to convert a CPU address into an extended address in such a way that said address operational processing causes said CPU address to be is converted into said extended address when said task it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

223. (Currently Amended) A storage medium storing therein a program as claimed in claim 151, wherein said program executes an address operational processing in order to convert a CPU address into an extended address in such a way that said address operational processing causes said CPU address to be is converted into said extended address when said task it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

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224. (Currently Amended) A storage medium storing therein a program as claimed in claim 152, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

225. (Currently Amended) A storage medium storing therein a program as claimed in claim 153, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

226. (Currently Amended) A storage medium storing therein a program as claimed in claim 154, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

227. (Cancelled)

228. (Cancelled)

229. (Currently Amended) A storage medium storing therein a program as claimed in claim 157, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address~~

~~operational processing causes~~ said CPU address ~~to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

230. (Currently Amended) A storage medium storing therein a program as claimed in claim 158, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address~~ ~~operational processing causes~~ said CPU address ~~to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

231. (Currently Amended) A storage medium storing therein a program as claimed in claim 159, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address~~ ~~operational processing causes~~ said CPU address ~~to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

232. (Currently Amended) A storage medium storing therein a program as claimed in claim 160, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address~~ ~~operational processing causes~~ said CPU address ~~to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

233. (Currently Amended) A storage medium storing therein a program as claimed in claim 161, wherein said program executes an address operational processing in

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order to convert a CPU address into an extended address in such a way that ~~said address operational processing causes~~ said CPU address ~~to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

234. (Cancelled)

235. (Cancelled)

236. (Currently Amended) A storage medium storing therein a program as claimed in claim 164, wherein said program executes an address operational processing in order to convert a CPU address into an extended address in such a way that ~~said address operational processing causes~~ said CPU address ~~to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

237. (Currently Amended) A storage medium storing therein a program as claimed in claim 165, wherein said program executes an address operational processing in order to convert a CPU address into an extended address in such a way that ~~said address operational processing causes~~ said CPU address ~~to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

238. (Currently Amended) A storage medium storing therein a program as claimed in claim 166, wherein said program executes an address operational processing in order to convert a CPU address into an extended address in such a way that ~~said address operational processing causes~~ said CPU address ~~to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area

A10 according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

239. (Currently Amended) A storage medium storing therein a program as claimed in claim 167, wherein said program executes an address operational processing in order to convert a CPU address into an extended address in such a way that ~~said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

240. (Currently Amended) A storage medium storing therein a program as claimed in claim 168, wherein said program executes an address operational processing in order to convert a CPU address into an extended address in such a way that ~~said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

241. (Cancelled)

242. (Cancelled)

243. (Currently Amended) A storage medium storing therein a program as claimed in claim 171, wherein said program executes an address operational processing in order to convert a CPU address into an extended address in such a way that ~~said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

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244. (Currently Amended) A storage medium storing therein a program as claimed in claim 172, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

245. (Cancelled)

246. (Currently Amended) A storage medium storing therein a program as claimed in claim 174, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

247. (Currently Amended) A storage medium storing therein a program as claimed in claim 175, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

248. (Currently Amended) A storage medium storing therein a program as claimed in claim 176, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area

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according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

249. (Currently Amended) A storage medium storing therein a program as claimed in claim 177, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

250. (Currently Amended) A storage medium storing therein a program as claimed in claim 178, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

251. (Cancelled)

252. (Cancelled)

253. (Currently Amended) A storage medium storing therein a program as claimed in claim 181, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

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254. (Currently Amended) A storage medium storing therein a program as claimed in claim 182, wherein said program executes an address operational processing in order to convert a CPU address into an extended address in such a way that ~~said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

255. (Currently Amended) A storage medium storing therein a program as claimed in claim 183, wherein said program executes an address operational processing in order to convert a CPU address into an extended address in such a way that ~~said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

256. (Currently Amended) A storage medium storing therein a program as claimed in claim 184, wherein said program executes an address operational processing in order to convert a CPU address into an extended address in such a way that ~~said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

257. (Currently Amended) A storage medium storing therein a program as claimed in claim 185, wherein said program executes an address operational processing in order to convert a CPU address into an extended address in such a way that ~~said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area

A10 according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

258. (Cancelled)

259. (Cancelled)

260. (Currently Amended) A storage medium storing therein a program as claimed in claim 188, wherein said program executes an address operational processing in order to convert a CPU address into an extended address in such a way that ~~said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

261. (Currently Amended) A storage medium storing therein a program as claimed in claim 189, wherein said program executes an address operational processing in order to convert a CPU address into an extended address in such a way that ~~said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

262. (Cancelled)

263. (Currently Amended) A storage medium storing therein a program as claimed in claim 191, wherein said program executes an address operational processing in order to convert a CPU address into an extended address in such a way that ~~said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area

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according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

264. (Currently Amended) A storage medium storing therein a program as claimed in claim 192, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

265. (Currently Amended) A storage medium storing therein a program as claimed in claim 193, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

266. (Currently Amended) A storage medium storing therein a program as claimed in claim 194, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

267. (Currently Amended) A storage medium storing therein a program as claimed in claim 195, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address operational processing causes said CPU address to be~~ is converted into said extended

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address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

268. (Cancelled)

269. (Cancelled)

270. (Currently Amended) A storage medium storing therein a program as claimed in claim 198, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

271. (Currently Amended) A storage medium storing therein a program as claimed in claim 199, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

272. (Currently Amended) A storage medium storing therein a program as claimed in claim 200, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

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273. (Currently Amended) A storage medium storing therein a program as claimed in claim 201, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address operational processing causes~~ said CPU address ~~to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

274. (Currently Amended) A storage medium storing therein a program as claimed in claim 202, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address operational processing causes~~ said CPU address ~~to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

275. (Cancelled)

276. (Cancelled)

277. (Currently Amended) A storage medium storing therein a program as claimed in claim 205, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address operational processing causes~~ said CPU address ~~to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

278. (Currently Amended) A storage medium storing therein a program as claimed in claim 206, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address~~

A10 ~~operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

279. (Cancelled)

280. (Currently Amended) A storage medium storing therein a program as claimed in claim 208, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address~~ ~~operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

281. (Currently Amended) A storage medium storing therein a program as claimed in claim 209, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address~~ ~~operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

282. (Currently Amended) A storage medium storing therein a program as claimed in claim 210, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address~~ ~~operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task it~~ is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

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283. (Currently Amended) A storage medium storing therein a program as claimed in claim 211, wherein said program executes an address operational processing in order to convert a CPU address into an extended address in such a way that ~~said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

284. (Currently Amended) A storage medium storing therein a program as claimed in claim 212, wherein said program executes an address operational processing in order to convert a CPU address into an extended address in such a way that ~~said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

285. (Cancelled)

286. (Cancelled)

287. (Currently Amended) A storage medium storing therein a program as claimed in claim 215, wherein said program executes an address operational processing in order to convert a CPU address into an extended address in such a way that ~~said address operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

288. (Currently Amended) A storage medium storing therein a program as claimed in claim 216, wherein said program executes an address operational processing in order to convert a CPU address into an extended address in such a way that ~~said address~~

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~~operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

289. (Currently Amended) A storage medium storing therein a program as claimed in claim 217, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address~~ ~~operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

290. (Currently Amended) A storage medium storing therein a program as claimed in claim 218, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address~~ ~~operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

291. (Currently Amended) A storage medium storing therein a program as claimed in claim 219, wherein said program executes an address operational processing in order to convert a CPU address into an extended address ~~in such a way that said address~~ ~~operational processing causes said CPU address to be~~ is converted into said extended address when ~~said task~~ it is judged that said task is permitted to access to said data area according to said access right judgement processing, before executing supporting processing by said memory with the extended address.

292. (Cancelled)

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(PATENT)

293. (Cancelled)